

12-31-'05 13:36 FROM-L/G/S LLP

+9549251101

T-670 P01/08_11-985

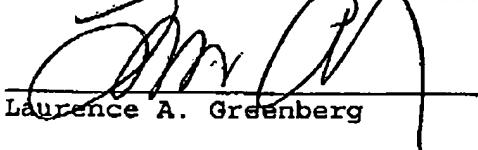
RECEIVED
CENTRAL FAX CENTER

Practitioner's Docket No.: P2002,0618

DEC 31 2005

CERTIFICATION OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 or facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below.


Laurence A. Greenberg

December 31, 2005
Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/623,843 Confirmation No.: 6526
Applicants : Veronika Polei, et al.
Filed : July 21, 2003
Title : METHOD FOR FABRICATING A BURIED BIT LINE
FOR A SEMICONDUCTOR MEMORY
Art Unit : 2823
Examiner : Khiem D. Nguyen
Customer No. : 24131

RESPONSE under 37 CFR § 1.116

Mail Stop Non Fee Amendment
Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the Office action dated October 31, 2005,
kindly consider the following remarks:

Remarks/Arguments begin on page 2 of this paper.